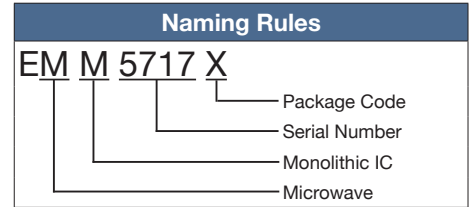


Ku to V Band Low Noise Amplifier MMICs

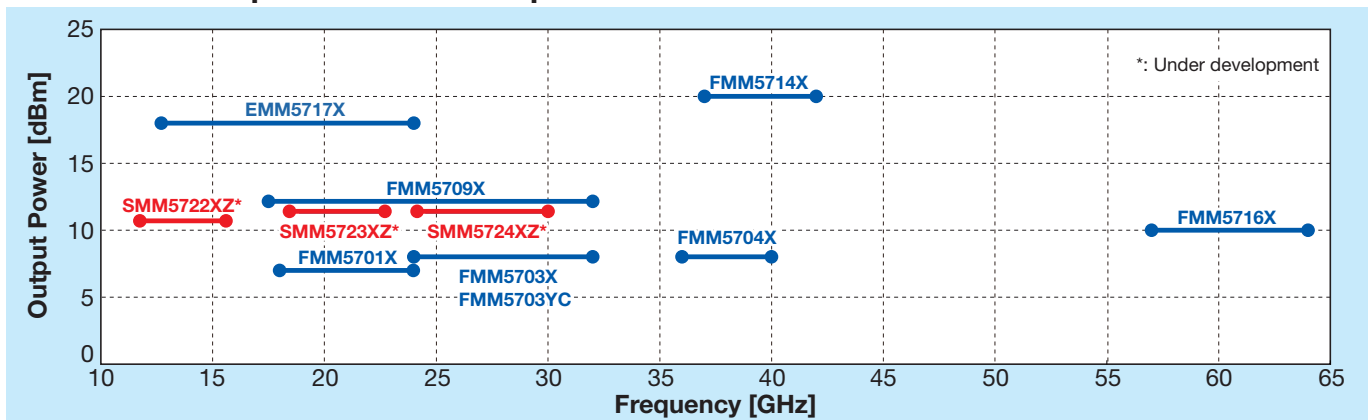
Sumitomo Electric provides GaAs Low Noise amplifier MMICs are designed for VSAT and radio link receiver applications. The performance of low noise figure and high associated gain are achieved using pHEMT technology and EB lithography process. Sumitomo Electric has line-ups of MMIC products specified from Ku-band through V-band.

Features

- Input and Output Internally Matched $Z_{in}/Z_{out} = 50\Omega$
- Low Noise Figure
- High Gain
- Wide Band
- High Reliability Bare Die (X)
- Low Cost Surface Mount Device (XZ)



◆ Low Noise Amplifier MMIC Lineup



Specifications

Ta=+25°C

Part Number	Frequency Range f (GHz)	Drain-Source Voltage VDD (V)	Noise Figure NF dB (Typ.)	Associated Gain Gas dB (Typ.)	Output Power at 1dB G.C.P. P1dB dBm (Typ.)	Drain Current IDD mA (Typ.)	Outline/Package Code	Application
SMM5722XZ	12–16	5	2.3	20	11	30	WLCSP	VSAT and Radio Link
EMM5717X	12.7–24	3	2.5	23	18	180	Chip	
FMM5709X	17.5–32	3	2.5	23	12.5	60	Chip	
SMM5723XZ*	18–23	5	2.7	20	12	30	WLCSP	
FMM5701X	18–28	5	1.5	13.5	7	12	Chip	
FMM5703X	24–32	3	2	18	9	20	Chip	
SMM5724XZ*	24–30	5	3.2	20	13	75	WLCSP	
FMM5704X	36–40	3	2	18	9	20	Chip	
FMM5714X	37–42	3	3	22	17 (f=37GHz) 20 (f=42GHz)	200	Chip	
FMM5716X	57–64	3	5	22	10	30	Chip	

G.C.P.: Gain Compression Point

*Under Development

Package Photo

